

USB

CPU

GPS

AC97

ETHERNET_A

ADC

ETHERNET_B

ETHERNET_C

GPIO

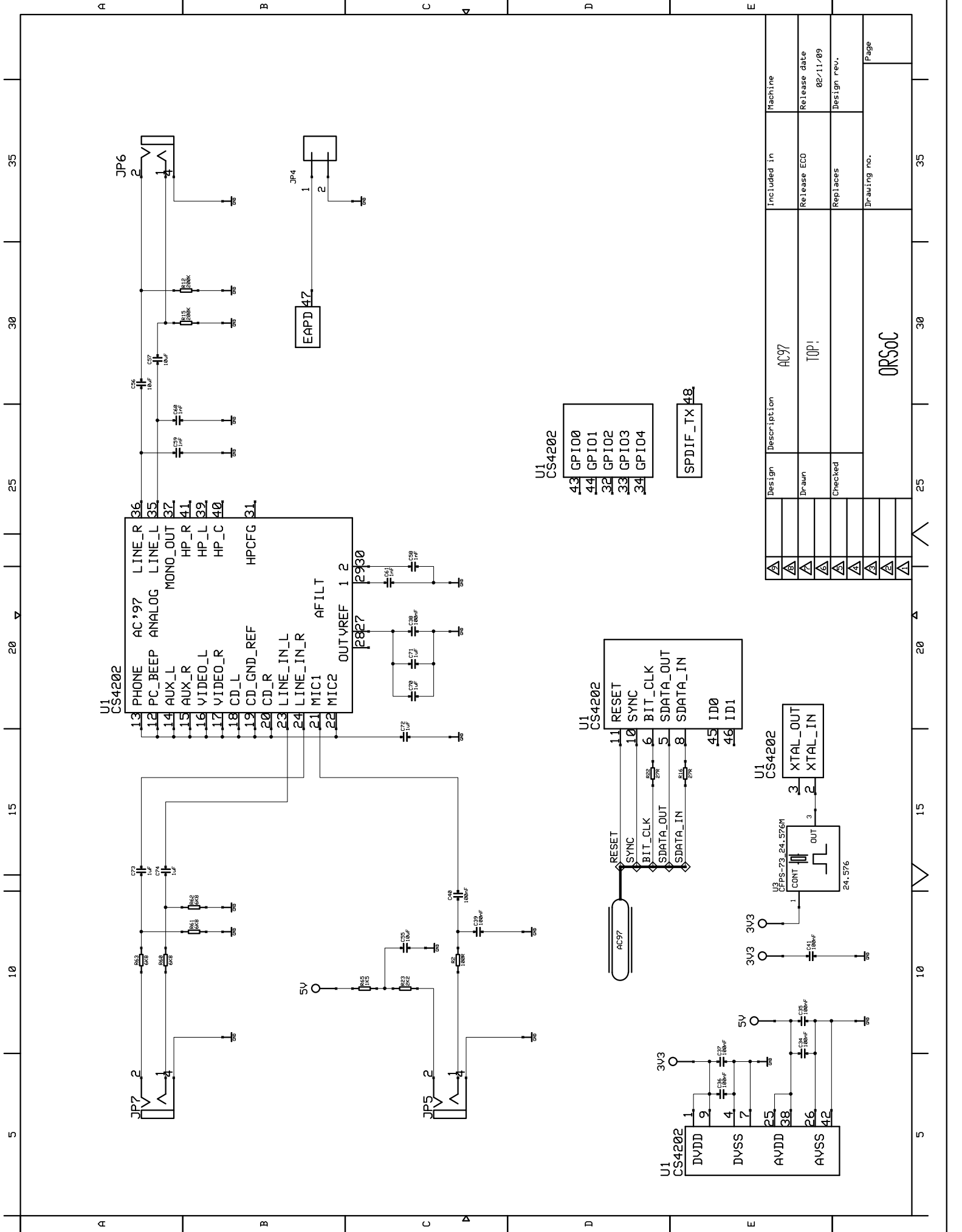
ETHERNET_D

DC/DC

RCS/LED

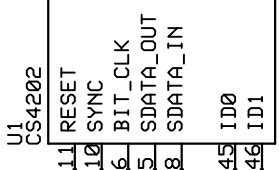
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△	Drawn	Replaces	Design rev.
△	Checked	Drawing no.	Page
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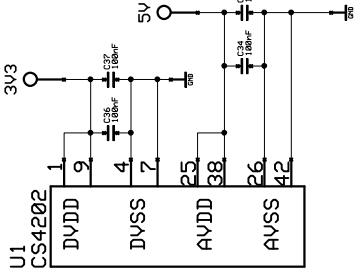
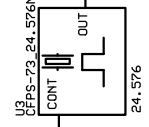
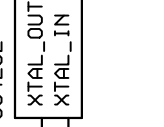


- U1 CS4202
- 43 GPI00
 - 44 GPI01
 - 32 GPI02
 - 33 GPI03
 - 34 GPI04

SPDIF_TX 4B

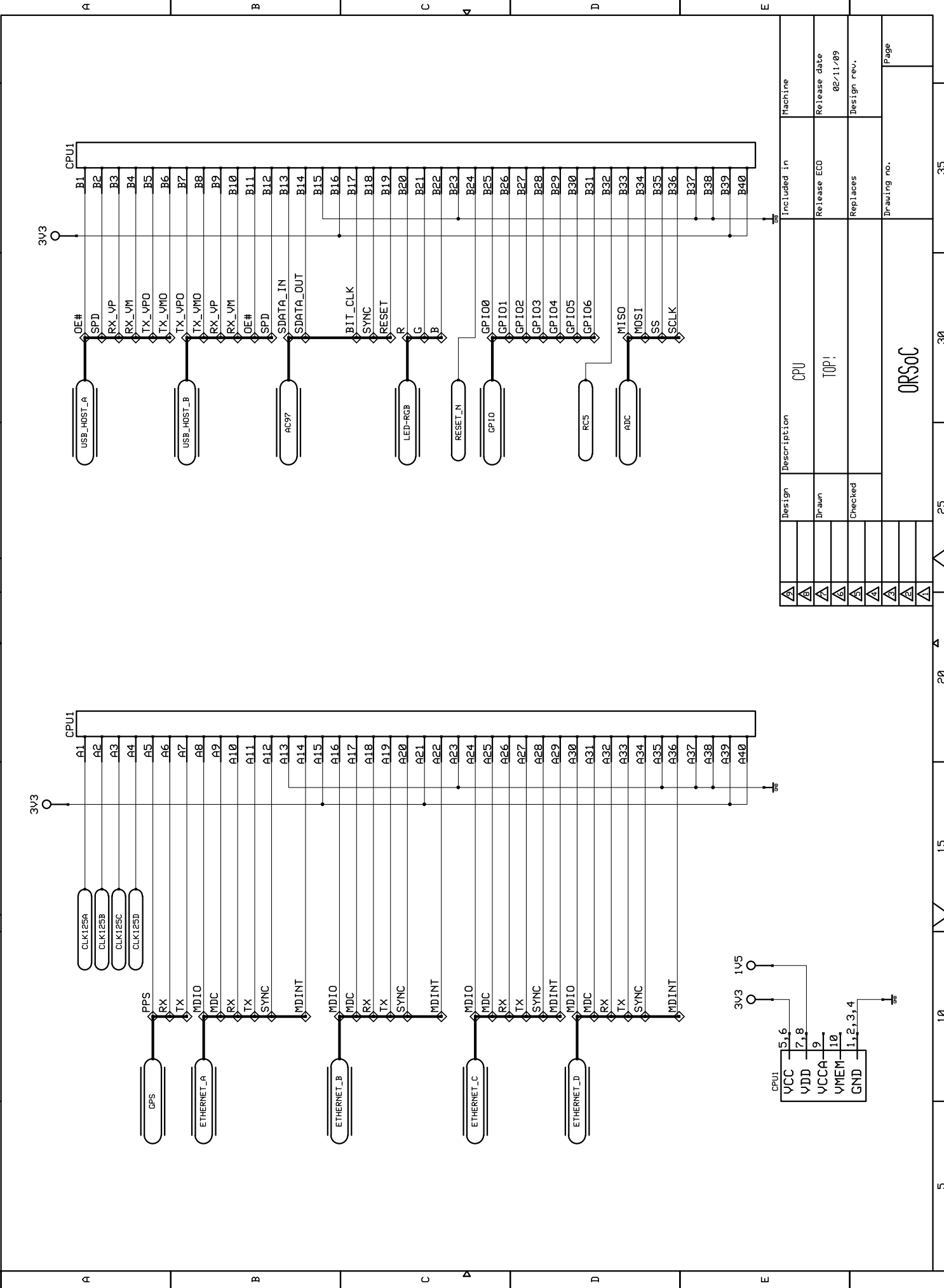


- U1 CS4202
- 11 RESET
 - 10 SYNC
 - 6 BIT_CLK
 - 5 SDATA_OUT
 - 8 SDATA_IN
 - 45 ID0
 - 46 ID1



Design	Description	Included in	Machine
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TOP!	TOP!	Replaces	02/11/09
Checked		Drawing no.	Design rev.
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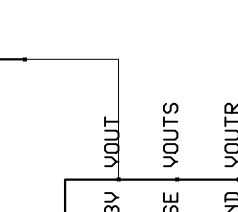
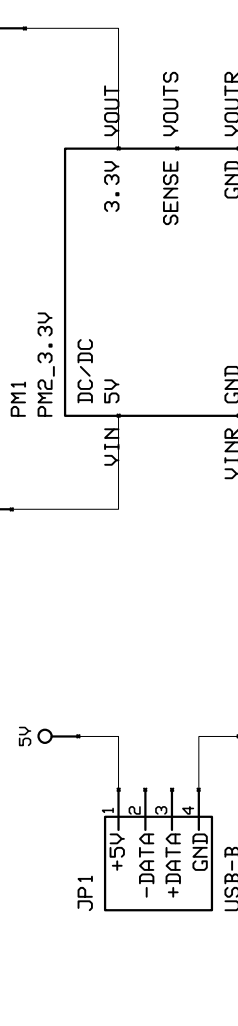
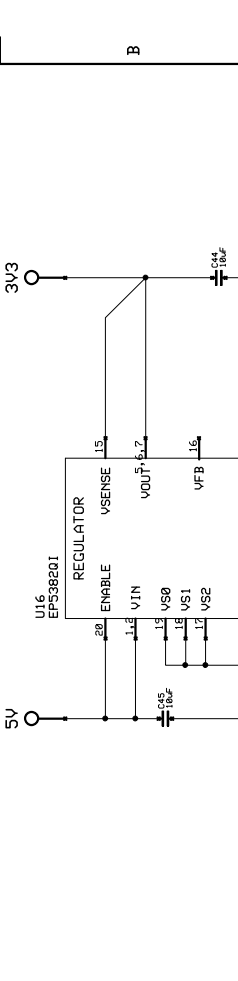
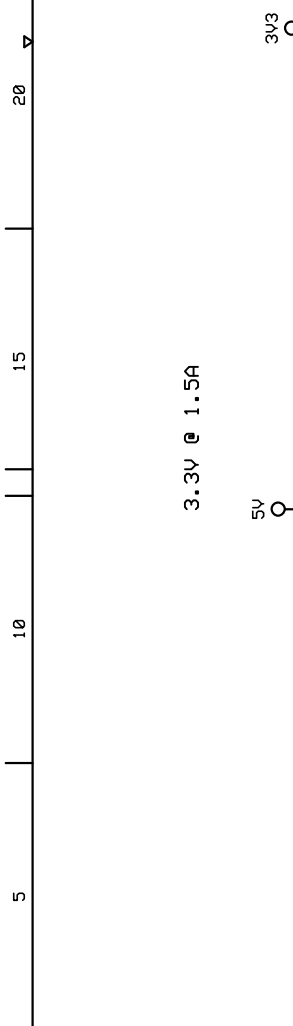
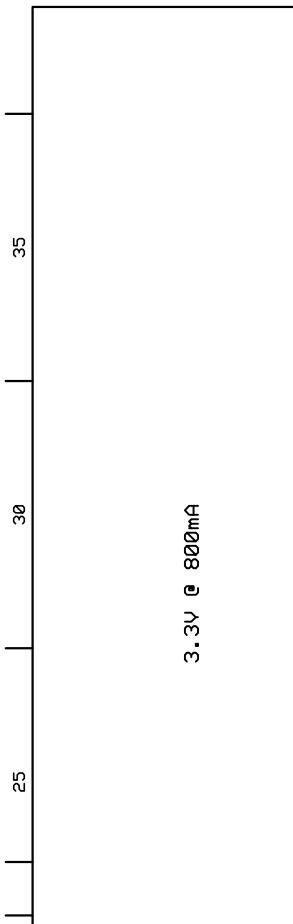
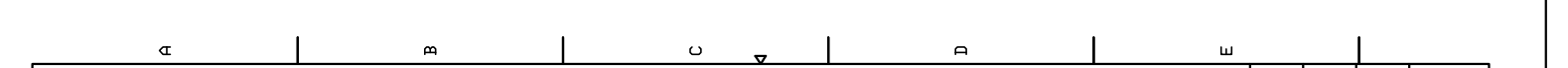


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▲			Replaces
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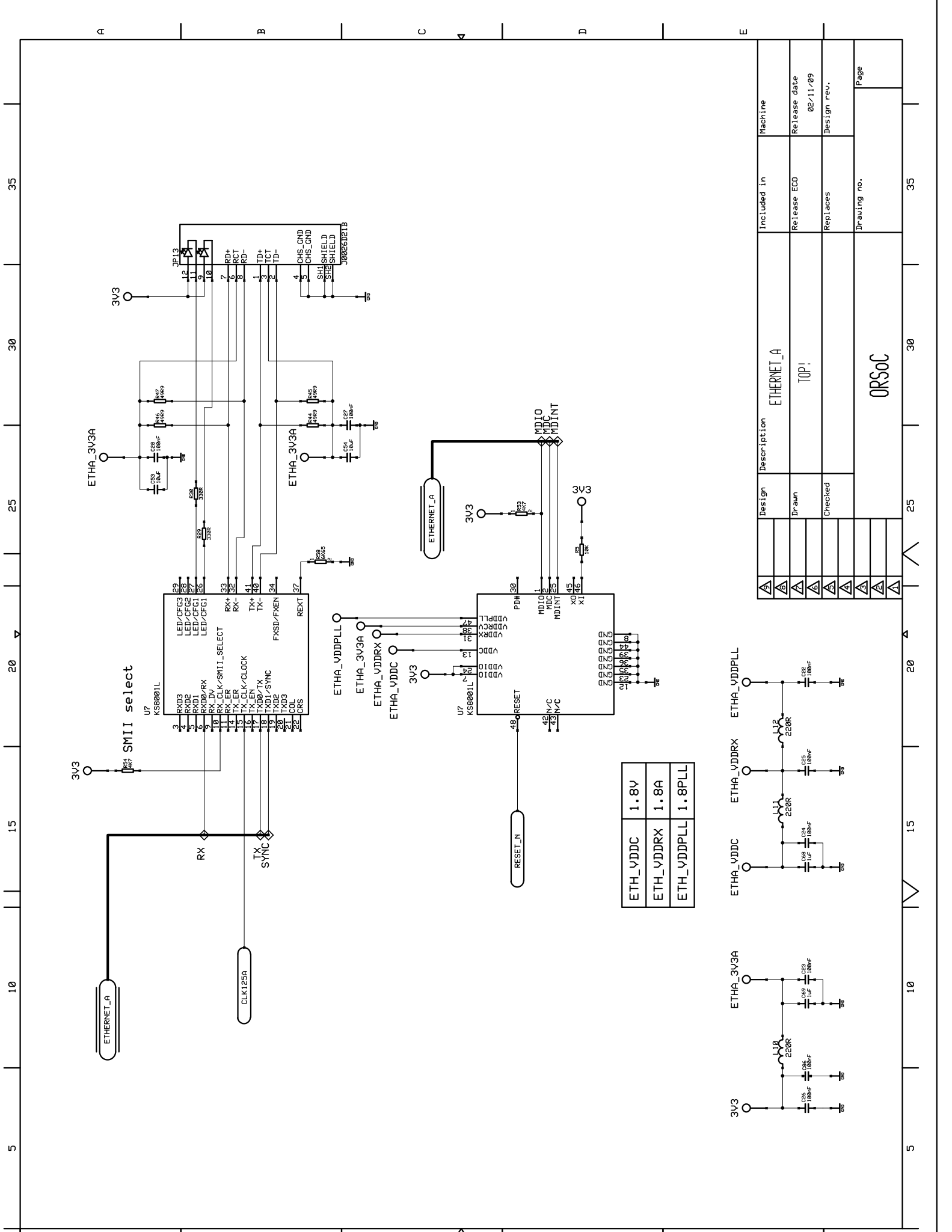
A B C D E

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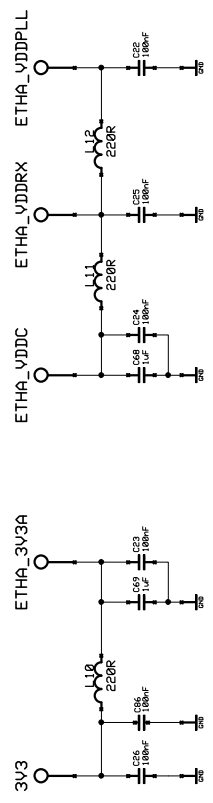


Design	Description	Included in	Machine
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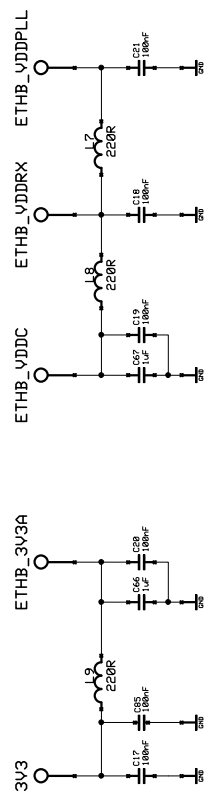
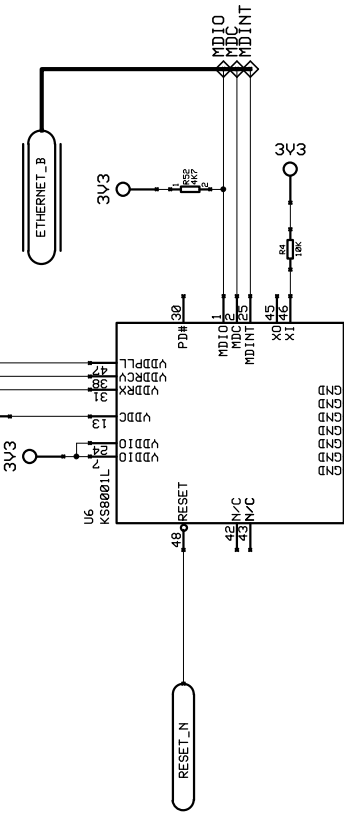
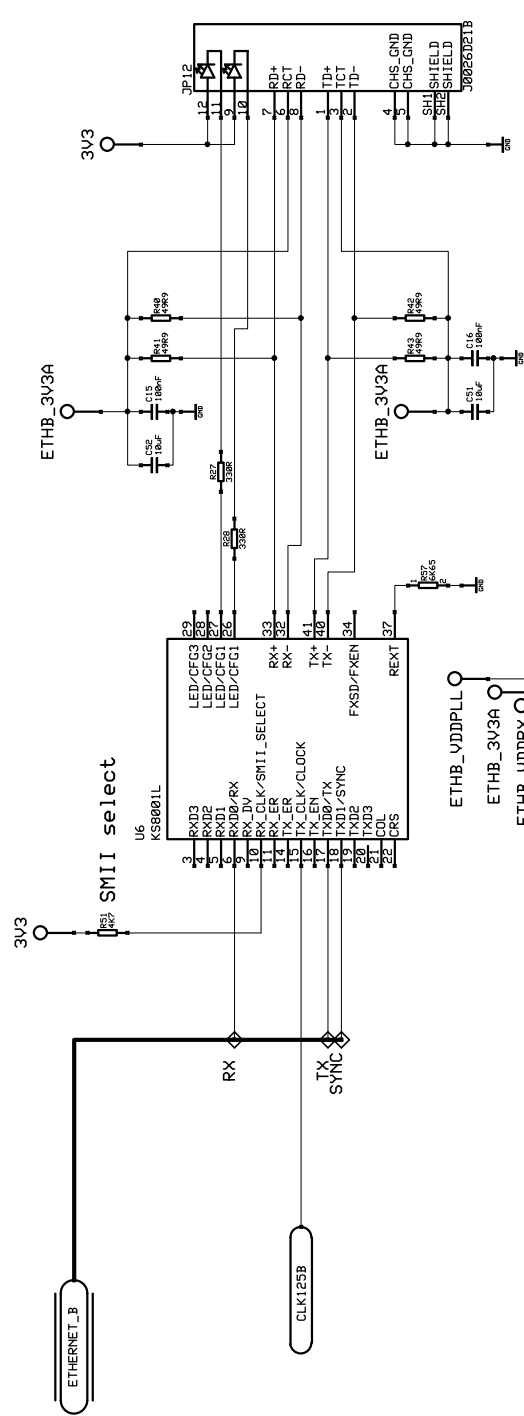


ETH_VDDC	1.8V
ETH_VDDR	1.8A
ETH_VDDPLL	1.8PLL



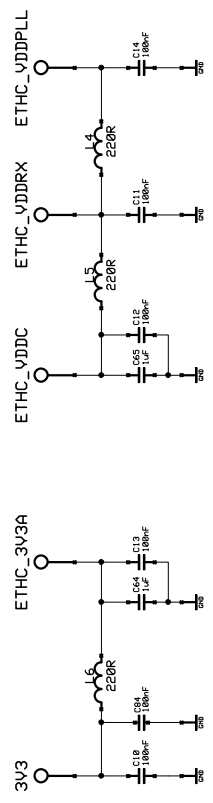
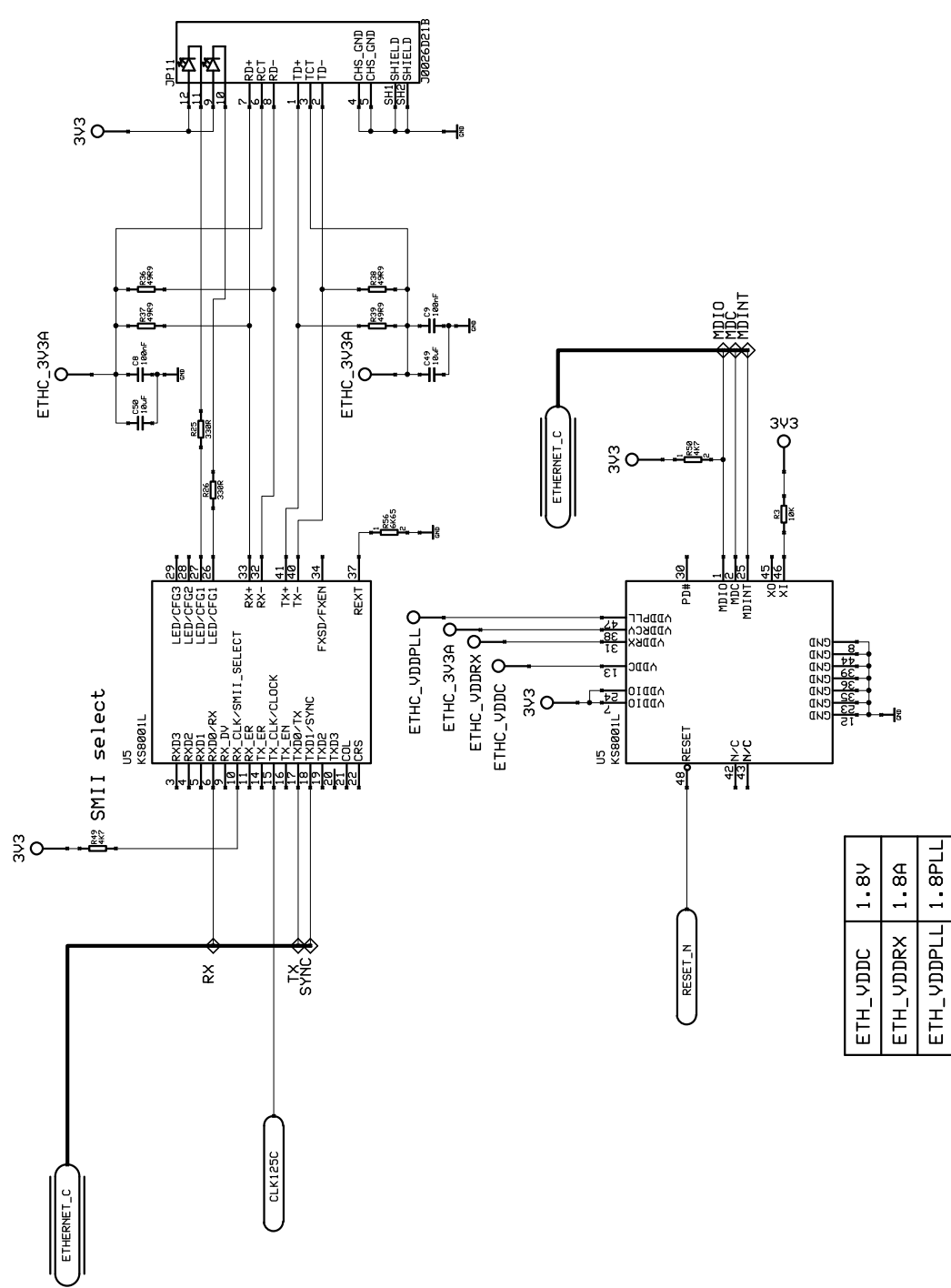
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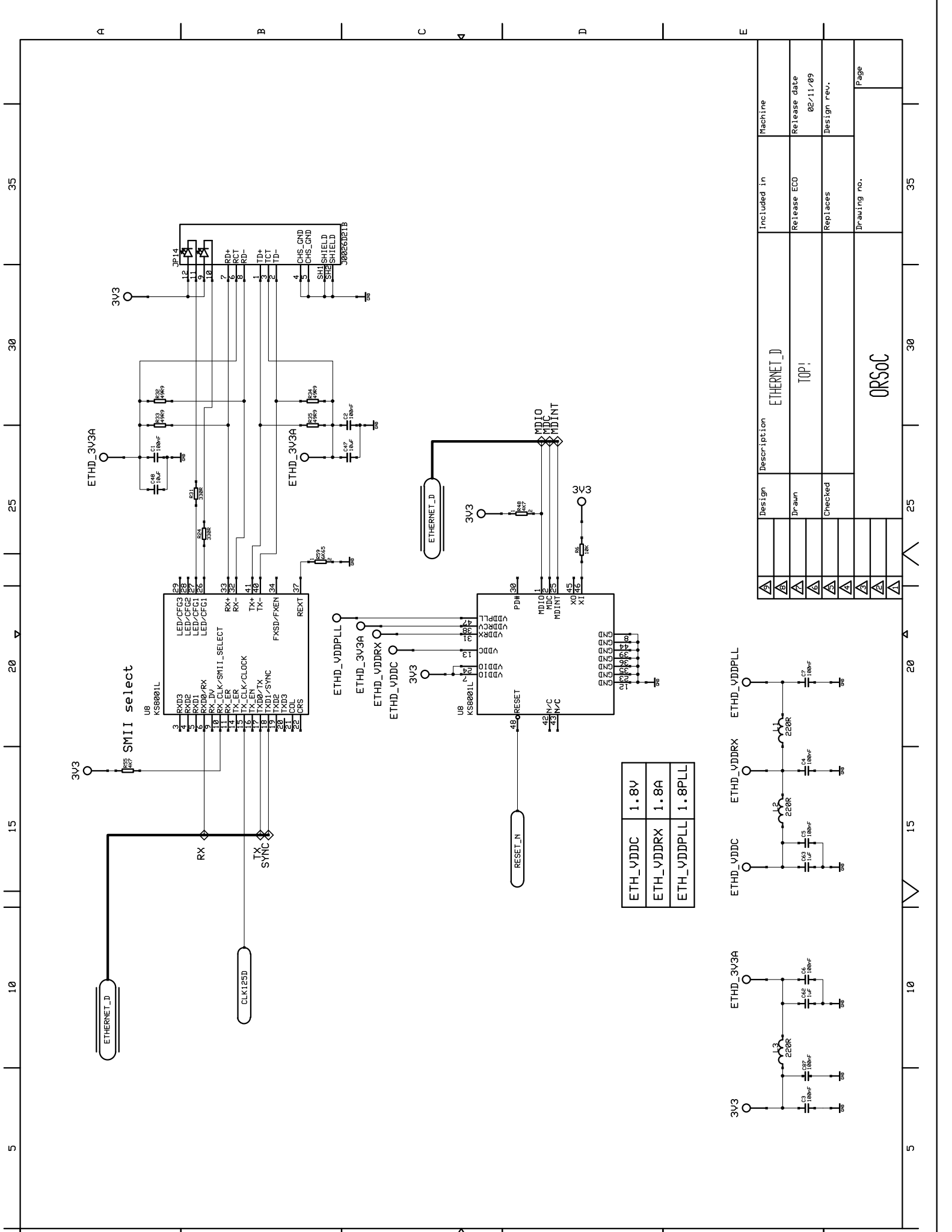
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Checked		Replaces	Design rev.
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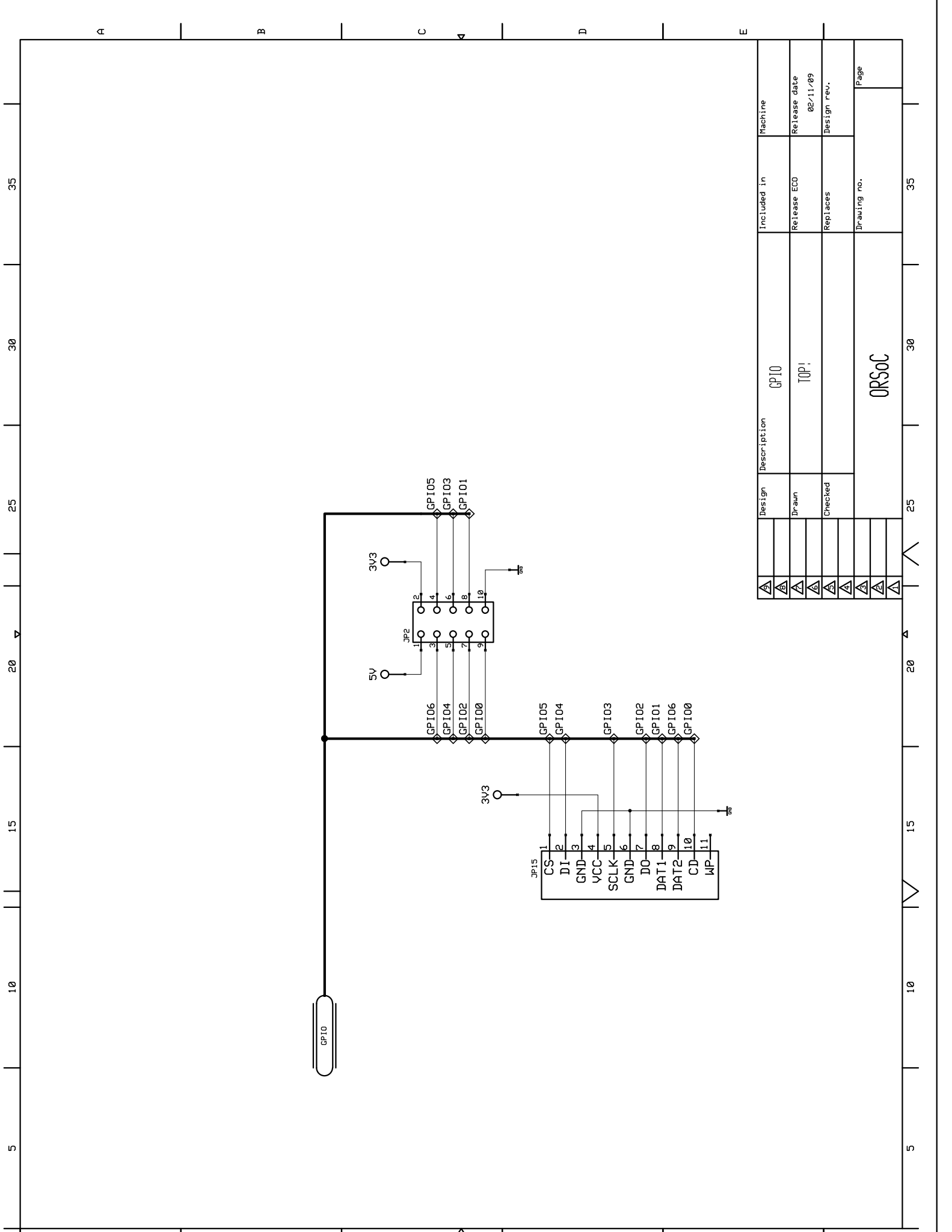
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ETH_VDDC	1.8V
ETH_VDDRX	1.8A
ETH_VDDPLL	1.8PLL

Design	Description	Included in	Machine
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Checked		Replaces	Design rev.
		Drawing no.	Page

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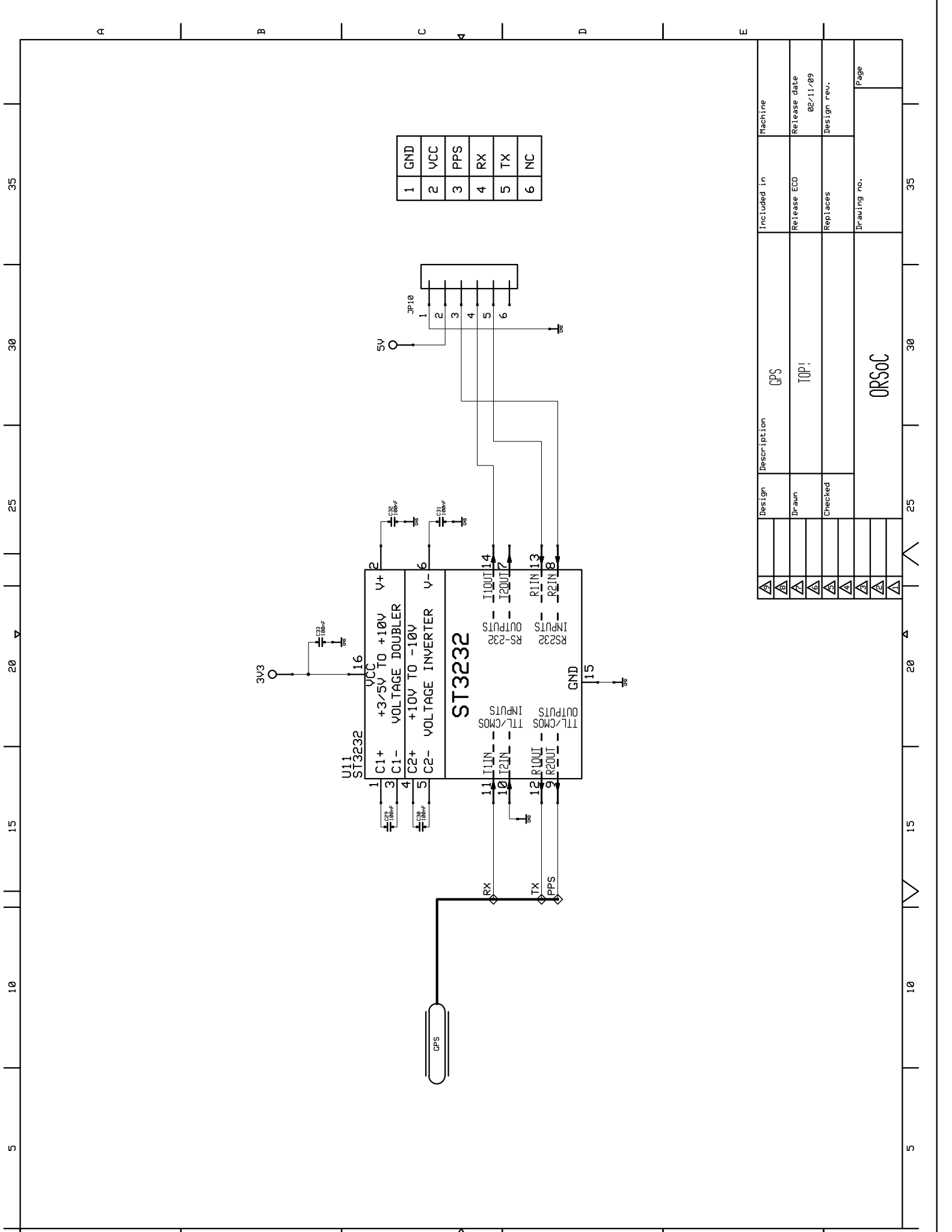
A B C D E

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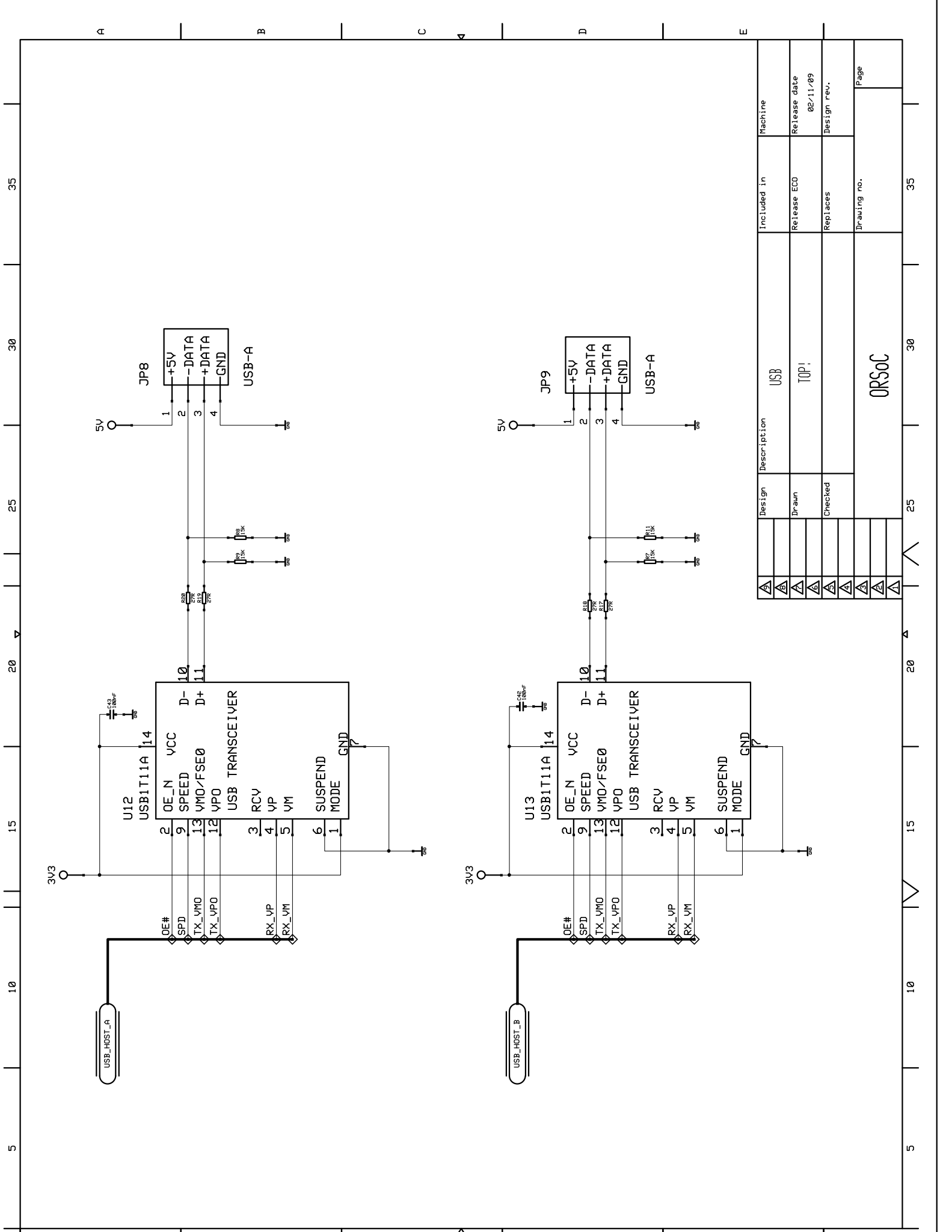
A B C D E



1	GND
2	VCC
3	PPS
4	RX
5	TX
6	NC

Design	Description	Included in	Machine
Design	GPS	Release ECO	Release date
Drawn	TOP!	Replaces	02/11/09
Checked		Drawing no.	Design rev.
			Page

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Design	Description	Included in	Machine
Drawn	USB	Release ECO	Release date
Checked	TOP!	Replaces	02/11/09
		Drawing no.	Design rev.
			Page
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